

REMARKS

Applicant has carefully reviewed this Application in light of the Office Action mailed September 21, 2006. Claims 1-21 are pending in this Application. Claims 1, 8, and 16 stand rejected under 35 U.S.C § 112 and Claims 1-21 stand rejected under 35 U.S.C § 103(a). Claims 1, 4, 6, 8, 9, 12-14, 16, 17, 20 and 21 have been amended to further define various features of Applicant's invention. Applicant respectfully requests reconsideration and favorable action in this case.

Claim Objections

Claims 4, 6, 8, 9, 12-14, 16, 17, 20 and 21 were objected to because of informalities. Applicant has amended Claims 4, 6, 8, 9, 12-14, 16, 17, 20 and 21 to overcome these objections.

Rejections under 35 U.S.C. § 112

Claims 1, 8 and 16 stand rejected by the Examiner under 35 U.S.C. §112, second paragraph, as being incomplete for omitting essential structural cooperative relationships of elements, such omission amounting to a gap between the necessary structural connections. Applicant has amended Claims 1, 8 and 16 to overcome these rejections. Applicant respectfully requests reconsideration and withdrawal of the rejections under 35 U.S.C. § 112 and full allowance of Claims 1, 8 and 16 as amended.

Rejections under 35 U.S.C. §103

Claims 1-21 stand rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,819,539 issued to David G. Wright et al. ("Wright").

Wright discloses a method for recovery from circuit stress conditions in a microcontroller. A microcontroller includes firmware to detect an event, including electrostatic discharge (ESD), electrical fast transient/burst (EFTB), radiated EMI and an undesired drop in voltage. (Col. 2, Lines 32-45). A detector detects the event and takes an appropriate action based on the event detected. (Col. 2, Lines 61-63). For example, firmware may perform a quick and complete reset after a detected stress event or, in the case of another event, the firmware may perform self checking, issue warnings, perform back-up operations,

cause a shut-down, or implement other recovery steps before or in place of a full reset. (Col. 2, Lines 63-67).

Claim 1 recites a method comprising “preventing the high voltage from propagating to the output of the delay circuit if the high voltage does not exist at the input after the predetermined amount of time.”

Claim 8 recites an information handling system comprising a “delay circuit configured to...prevent a high voltage from propagating to the output of the delay circuit if the high voltage does not exist at the input after the predetermined amount of time.”

Claim 16 recites a device comprising a “delay circuit configured to...prevent a high voltage from propagating to the output if the high voltage does not exist at the input after the predetermined amount of time.”

Applicant respectfully submits that the cited reference fails to disclose every element of Applicant’s invention. *Wright* fails to teach or suggest at least a method comprising “preventing the high voltage from propagating to the output of the delay circuit if the high voltage does not exist at the input after the predetermined amount of time,” as recited by amended Claim 1. *Wright* also fails to teach or suggest an information handling system comprising a “delay circuit configured to...prevent a high voltage from propagating to the output of the delay circuit if the high voltage does not exist at the input after the predetermined amount of time,” as recited by amended Claim 8. In addition, *Wright* fails to disclose a device comprising a “delay circuit configured to...prevent a high voltage from propagating to the output if the high voltage does not exist at the input after the predetermined amount of time,” as recited by amended Claim 16. The cited reference, therefore, fails to disclose the recited limitations and cannot render obvious Claims 1, 8, and 16.

Given that Claims 2-7 depend from Claim 1, Claims 9-15 depend from Claim 8, and Claims 17-21 depend on Claim 16, Applicant respectfully submit that Claims 2-7, 9-15, and 17-21 are allowable. As such, Applicant respectfully request that the Examiner withdraw and rejection and allow Claim 2-7, 9-15, and 17-21.

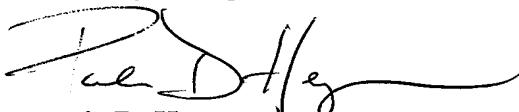
CONCLUSION

Applicant appreciate the examiner's careful review of the application. Applicant has made an earnest effort to place this case in condition for allowance in light of the amendments and remarks set forth above. For the foregoing reasons, Applicant respectfully request reconsideration of the rejections and full allowance of Claims 1-21, as amended.

Applicant believes there are no fees due at this time, however, the Commissioner is hereby authorized to charge any fees necessary or credit any overpayment to Deposit Account No. 02-0383 of Baker Botts L.L.P.

If there are any matters concerning this Application that may be cleared up in a telephone conversation, please contact Applicant's attorney at 512.322.2581.

Respectfully submitted,
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Date: Dec. 21, 2006

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